

Analog, Mixed-signal, RF, Power Management IC Design Positions

**Fujitsu Semiconductor Wireless Products
2100 E. Elliot Road, Tempe AZ 85283**

We are seeking highly motivated candidates to fill IC design positions in our transceiver design organization. The ideal candidate will provide expertise in one or more of the following areas: analog, mixed-signal, RF and power management.

Qualifications:

- 1. Experience in the design of CMOS integrated circuits such as:**
 - **Analog Circuits:** operational amplifiers, active filters, DC regulators, analog-to-digital, digital-to-analog converters
 - **RF Circuits:** LNAs, mixers, RF amplifiers, PLLs, quadrature generators
 - **Power Management Circuits:** switching regulators, buck-boost converters
- 2. Knowledge of wireless transmitters and receivers a plus**
- 3. Experience in using Cadence design tools, SPICE, and behavioral modeling**
- 4. Programming skills: Matlab, Verilog A, Verilog AMS**
- 5. Ph.D. or M.Sc. + 3 years**

Responsibilities:

- 1. Specify and document circuit specifications**
- 2. Design, simulation of novel, fully integrated high performance circuits**
- 3. Interaction with layout designers to realize high quality designs**
- 4. Verification of performance in the system and bench evaluation**

Please Send Resumes to: FSWP_RF@us.fujitsu.com

Founded in 2010 and headquartered in Tempe, Arizona, Fujitsu Semiconductor Wireless Products (FSWP), is a wholly owned subsidiary of Fujitsu Semiconductor Limited in Japan. FSWP develops advanced multimode RF transceiver products for 2G/3G/4G cellular handsets.

Equal Opportunity: All qualified applicants will receive consideration for employment without regard to race, color, religion, gender, gender identity or expression, sexual orientation, national origin, genetics, disability, age, or veteran status.